

Amendments to the Specification

In the Abstract, please amend the following.

~~The invention is concerned with an integrated circuit chip comprising, in sequence, a substrate layer of a substrate material, an insulating layer of an insulating material, a first electrically conductive layer of a first electrically conductive material, a dielectric layer of a dielectric material and a second electrically conductive layer of a second electrically conductive material, said integrated circuit chip comprising at least one integrated circuit and at least one integrated electrostatic discharge protection device, said electrostatic discharge protection device comprising a pair of spaced center and circumferential electrodes, the center electrode being formed by the first electrically conductive layer and the circumferential electrode being formed by the second electrically conductive layer, said electrodes being separated by a steroidal spark gap cavity, wherein the toroid of the steroidal spark gap cavity comprises a base layer formed by the insulating layer of the integrated circuit chip, a side wall formed by the circumferential electrode, a cover layer formed by the dielectric layer of the integrated circuit chip, and the center of the toroid being formed by the center electrode comprising a contact pad in contact with the insulating layer, said electrostatic discharge protection device also comprising means to electrically connect the center electrode to input circuit paths to be protected from electrostatic discharge and means to electrically connect the circumferential electrode to an electrostatic discharge path comprising either a connection to a circuit ground or a circuit supply voltage. The invention is also concerned with a method of making such integrated circuit chip.~~

Consistent with an example embodiment, in integrated circuit chip includes an electrostatic discharge(ESD) protection device. A feature of the ESD protection device includes a pair of spaced center and circumferential electrodes, the center electrode being formed by the first electrically conductive layer and the circumferential electrode being formed by the second electrically conductive layer, said electrodes being separated by a steroidal spark gap cavity.

Amendments to the Claims

1. (*Currently Amended*) An integrated circuit chip comprising, in sequence,
_____ a substrate layer of a substrate material,
_____ an insulating layer of an insulating material,
_____ a first electrically conductive layer of a first electrically conductive material,
_____ a dielectric layer of a dielectric material and
_____ a second electrically conductive layer of a second electrically conductive material,
said IC chip comprising at least one integrated circuit and at least one integrated
electrostatic discharge protection device, said electrostatic discharge protection device
comprising,
a pair of spaced center and circumferential electrodes, the center electrode
being formed by the first electrically conductive layer and the circumferential electrode
being formed by the second electrically conductive layer, said electrodes being separated
by a steroidal spark gap cavity, wherein the toroid of the steroidal spark gap cavity
comprises,
_____ a base layer formed by the insulating layer of the integrated circuit
chip,
_____ a side wall formed by the circumferential electrode,
_____ a cover layer formed by the dielectric layer of the integrated circuit
chip, and the center of the toroid being formed by the center electrode comprising a
contact pad in contact with the insulating layer,
_____ said electrostatic discharge protection device also comprising means to
electrically connect the center electrode to input circuit paths to be protected from
electrostatic discharge and means to electrically connect the circumferential electrode to
an electrostatic discharge path comprising either a connection to a circuit ground or a
circuit supply voltage.

2. (*Original*) The integrated circuit chip of claim 1, further comprising a passive
component selected from the group comprising resistors, capacitors, and inductors.

3. *(Original)* The integrated circuit chip of claim 1, wherein the first electrically conductive material is polysilicon.
4. *(Original)* The integrated circuit chip of claim 1, wherein the second electrically conductive material is aluminum.
5. *(Original)* The integrated circuit chip of claim 1, wherein the spark gap cavity contains a noble gas for reducing the breakdown voltage of the electrostatic discharge protection device.
6. *(Original)* The integrated circuit chip of claim 1, wherein the substrate material is selected from the group comprising silicon, glass and a ceramic material.
7. *(Currently Amended)* A method of fabricating an integrated circuit chip comprising an integrated circuit and an electrostatic discharge protection device ~~according to the invention, including~~ comprising the steps of
- _____ a) providing a semiconductor substrate,
 - _____ b) depositing an insulating layer on semiconductor substrate,
 - _____ c) depositing a first electrically conductive layer of a first electrically conductive material on said insulating layer,
 - _____ d) depositing a dielectric layer of a dielectric material on said first electrically conductive layer,
 - _____ e) etching spaced contact windows for a center electrode and a circumferential electrode,
 - _____ f) depositing a mask,
 - _____ g) etching a hollow groove into the first electrically conductive layer under the circumference of the contact window of the circumferential electrode,
 - _____ h) depositing a layer of a second electrically conductive layer through the contact window of the center electrode to mechanically contact the insulating layer, and through the contact window of the circumferential electrode to electrically contact the first electrically conductive layer, and

_____i) connecting the center electrode to input circuit paths to be protected from electrostatic discharge and connecting the circumferential electrode to an electrostatic discharge path comprising either a connection to a circuit ground or a circuit supply voltage.